

## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/772,499	01/29/2001	Alan Gatherer	TI-30024	1361	
7590 12/02/2004			EXAMINER		
RONALD O. NEERINGS			VARTANIAN, HARRY		
	ents Incorporated	ART UNIT	PAPER NUMBER		
Mail Station 3999 P.O. Box 655474				FAFER NUMBER	
Dallas, TX 75	, :		2634		
, · · · · · · · · · · · · ·			DATE MAILED: 12/02/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	ı No.	Applicant(s)	
	085 - A-41 - 0	09/772,499		GATHERER ET AL.	
	Office Action Summary	Examiner		Art Unit	<b>k</b> /
		Harry Varta		2634	<u> </u>
Period f	The MAILING DATE of this communication aport Reply	ppears on the	cover sheet with the d	orrespondence add	dress
THE - Extended aftended aftend	MORTENED STATUTORY PERIOD FOR REP MAILING DATE OF THIS COMMUNICATION ensions of time may be available under the provisions of 37 CFR 1 or SIX (6) MONTHS from the mailing date of this communication. The period for reply specified above is less than thirty (30) days, a result of period for reply is specified above, the maximum statutory period or reply within the set or extended period for reply will, by staturely received by the Office later than three months after the mail need patent term adjustment. See 37 CFR 1.704(b).	I. 1.136(a). In no even eply within the statut d will apply and will ute, cause the applic	t, however, may a reply be tin ory minimum of thirty (30) day expire SIX (6) MONTHS from ation to become ABANDONE	nely filed s will be considered timely the mailing date of this co D (35 U.S.C. § 133).	, mmunication.
Status					
1)⊠	Responsive to communication(s) filed on 13.	August 2004.			
2a)□	This action is <b>FINAL</b> . 2b)⊠ Th	nis action is no	n-final.		
3)	Since this application is in condition for allow	ance except for	or formal matters, pro	secution as to the	merits is
	closed in accordance with the practice under				:
Disposi	tion of Claims				
4) 又	Claim(s) 1 and 3-14 is/are pending in the app	plication.	,		:
,	4a) Of the above claim(s) is/are withdr		sideration.		;
5)	Claim(s) is/are allowed.		•		;
6)⊠	Claim(s) 1 and 3-14 is/are rejected.				:
7)	Claim(s) is/are objected to.				:
8)	Claim(s) are subject to restriction and	l/or election re	quirement.		
Applicat	ion Papers				:
9)[	The specification is objected to by the Examir	ner.			
10)	The drawing(s) filed on is/are: a) ac	ccepted or b)	objected to by the I	Examiner.	:
	Applicant may not request that any objection to th	ne drawing(s) be	held in abeyance. See	e 37 CFR 1.85(a).	
	Replacement drawing sheet(s) including the corre	ection is require	d if the drawing(s) is ob	jected to. See 37 CF	R 1.121(d).
11)	The oath or declaration is objected to by the B	Examiner. Not	e the attached Office	Action or form PT	O-152.
Priority	under 35 U.S.C. § 119				<u>:</u>
	Acknowledgment is made of a claim for foreig ☐ All b)☐ Some * c)☐ None of:			)-(d) or (f).	
	1. Certified copies of the priority documen				
	2. Certified copies of the priority documer		• •	<u> </u>	Ctome
	3. Copies of the certified copies of the pri application from the International Bure			a in this National	Stage
*	See the attached detailed Office action for a lis	st of the certific	ed copies not receive	ed.	:
					:
Attachme	nt/c)				:
_	ce of References Cited (PTO-892)		4) 🔀 Interview Summary	(PTO-413)	:
2) 🔲 Noti	ce of Draftsperson's Patent Drawing Review (PTO-948)		Paper No(s)/Mail Da	ate	:
	rmation Disclosure Statement(s) (PTO-1449 or PTO/SB/0i er No(s)/Mail Date <u>9/2004</u> .	,	5)	atent Application (PTO	)-152)
ı ap	5, 110(0) 111011 Date <u>0/2007</u> .	·	-,		

Art Unit: 2634

## **DETAILED ACTION**

Claims 1 and 3-14 are pending.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

1. Claims 1, 3-7 are rejected under 35 U.S.C. 102(b) as being anticipated by Halter et al(IEEE 10/1998). Regarding Claim 1, Halter et al meets the limitations of the claim by disclosing a pipelined(Pg. 267-268, section 4.2) Viterbi decoding method using forward and backward sliding window operations(Table 1, Pg. 268) wherein state metrics are derived and stored in memory for each window, i.e. partial block, in parallel(Pg. 269, section 4.3.2).

Regarding Claim 3, the description of the pipelined Viterbi decoding above meets the limitations of the claim that the sequential processing is done in two directions. Regarding the use of prologs, the applicant defines that a prolog "consists of the several bits to the left of the sliding window. This is shown by the **overlap between successive sliding blocks** in the figure"(Pg. 14, para 2). Halter et al state that a state metric can be calculated at "an intermediate starting point "k" along some window length "L"(Pg. 267, section 4.2). Therefor he says that the recursion does not start from the "last time step of the trellis" (Pg. 267, section 4.2) indicating that there is overlap among the windows.

Art Unit: 2634

Regarding Claim 4, Halter et al meets the limitations of the claim by disclosing that the state metrics are derived and stored in memory for each window, i.e. partial block, in parallel(Pg. 269, section 4.3.2).

Regarding Claim 5, Halter et al meets the limitations of the claim by disclosing that the processing of the blocks of windowed data is also pipelined(Pg. 268, paragraph 2).

Regarding Claim 6, the rejection for claim 1 above meets the limitations of the claim.

The additional limitation of performing steps a) and b) in parallel is meet with the specific disclosure of state metric calculation

Regarding Claim 7, Halter et al meets the limitations of the claim by disclosing that the processing of the blocks of sliding window data is also pipelined(Pg. 268, paragraph 2).

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

The factual inquiries set forth in *Graham* **v.** *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.
- 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 2. Claims 8-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Halter et al(IEEE 10/1998) in view of Abbaszadeh (US Patent 6,563,877). Regarding Claim 8,

Application/Control Number: 09/772,499 Page 4

Art Unit: 2634

Halter et al meets all the limitations of Claim 8(see above paragraphs) including the use of an adder(Pg. 270, section 4.3.2.1) tree(Pg. 271, figure 7) in the state metric calculation of pipelined parallel Viterbi decoding. Halter et al fails to teach the use of a maximum finding operation.

However, Abbaszadeh discloses the use of "maximum finding" among trellis states that includes the use of a normalization block(Column 6, Line 63 to Column 7, Line 26). Therefor it would have been prima facie obvious to use a maximum finding operations in a MAP decoding method. A motivation to combine is that it is well known in the art that finding a maximum state metric is a common step in Viterbi and Trellis decoding. For instance, maximum finding is used in Maximum Likelihood Viterbi decoding as stated by Abbaszadeh(Column 2, lines 47-53).

Regarding Claims 9 and 12, Abbaszadeh meets the following limitations:

wherein the maximum-finding operation is an exponent-logarithm equation. (Column 5, lines 31 -35)

Regarding Claims 10 and 13, Abbaszadeh meets the following limitations:

wherein the maximum-finding operation is an estimation of an exponent-logarithm function.(Column 5, lines 31 -35)

Regarding Claim 11, Abbaszadeh discloses the use of a "normalization operation on the results" in figures 8 and 9. Please also see Column 6, Line 63 to Column 7, Line 26 for the specifics.

3. Claim 14 is rejected under 35 U.S.C. 103(a) as being unpatentable over Halter et al(IEEE 10/1998) in view of Van Stralen et al (US Pat #6,304,996). Halter et al meets all the limitations of Claim 14(see above paragraphs), except disclosing an alpha and beta generation process in their Map decoding systems.

However, Van Stralen et al meets the following limitations of the Claim:

Art Unit: 2634

an alpha generation process; (Column 1, Line 45 to (Column 2, Lines 5)

a beta generation process; (Column 1, Line 45 to Column 2, Lines 5)

Therefor, it would have been prima facie obvious to a person having ordinary skill in the art to which said subject matter pertains to use an alpha and beta generator in a pipelining, parallel, sliding window MAP decoder. The motivation to combine is stated by Van Stralen et al. They state the alpha and beta processes are essentially equivalent to the forward and backward recursions stated by Halter et al. More specifically, he states:

"Three fundamental terms in the MAP algorithm are the forward and backward state probability functions (the alpha and beta functions, respectively) and the a posteriori transition probability estimates (the sigma function)." (Column 1, Lines 22-36)

The motivation to do the alpha and beta operations in parallel are also stated by Van Stralen et al:

"Two gamma probability function values are provided via selection switches to the alpha and beta blocks for calculating the alpha and beta probability function values, i.e., performing the alpha and beta recursions, respectively, in parallel, thus significantly increasing decoding speed." (Column 1, Line 45 to Column 2, Lines 5)

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Harry Vartanian whose telephone number is 571.272.3048. The examiner can normally be reached on 10:00-6:30 Mondays to Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Stephen Chin can be reached on 571.272.3056. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2634

Page 6

Application Information Retrieval (PAIR) system. Status information for published

Information regarding the status of an application may be obtained from the Patent

applications may be obtained from either Private PAIR or Public PAIR. Status information

for unpublished applications is available through Private PAIR only. For more information

about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on

access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-

217-9197 (toll-free).

Harry Vartanian Examiner

Art Unit 2634

HV

STEPHEN CHIN

SUPERVISORY PATENT EXAMINE

TECHNOLOGY CENTER 2600